

# LINDY®

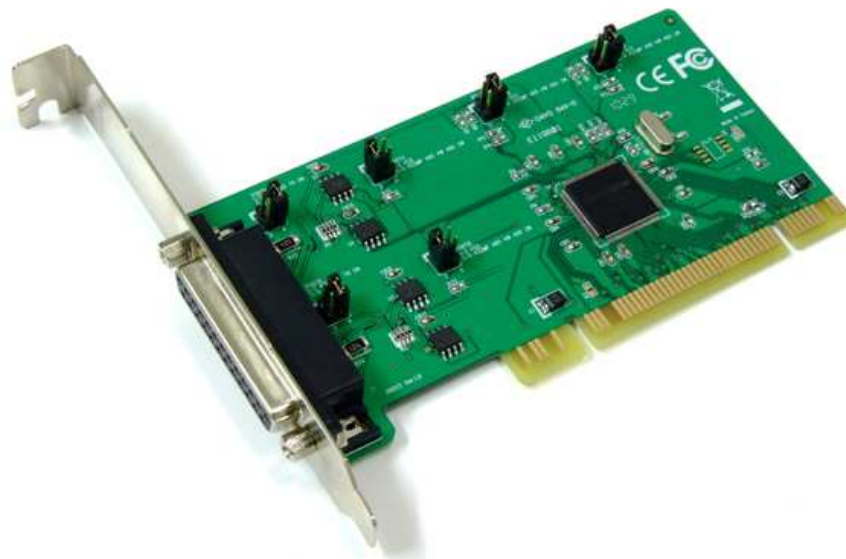
## CONNECTION PERFECTION

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### RS-422/485 PCI Card

User Manual

*English*



LINDY No. 51215 (2 Port)

[www.lindy.com](http://www.lindy.com)



## ◆ 1.0 Introduction

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Thank you for purchasing the **LINDY RS-422/485 Controller**. This PCI serial card enables your computer to support serial RS-422/485 ports with 256Kbyte FIFO.

## ◆ 2.0 Packing List

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Please check if the following items are present and in good condition upon opening the packaging. Contact your vendor if any items are damaged or missing.

- Hardware:

Serial Communication Board: LINDY PCI Host Controller x 1

Cables:

2 Port, **No.51215** : DB25 Male to 2 Port DB9

- Driver CD
- This User Manual

## ◆ 3.0 Features

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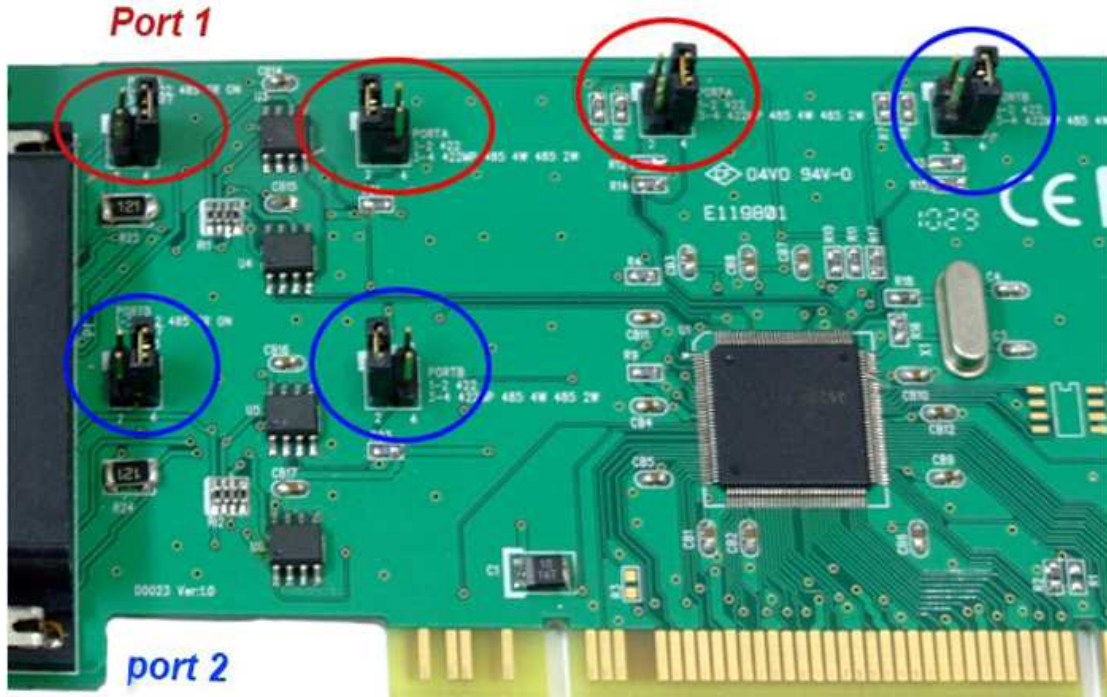
- Communication speed Maximum 921.6K BPS
- BUS interface PCI Local Bus Spec 2.2
- Circuit interface RS422/RS485
- Communication controller 16C1052
- Connector DB9
- I/O address Automatic set-up
- IRQ number Automatic set-up
- Circuit Protection ESD Protector attached
- Supporting operating system Windows 98/2000/NT/XP, Linux

## ◆ 4.0 Mode Change

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Select between point-to-point mode and multi drop mode during the installation of the device driver.

The default is point-to-point mode.



Port 1 –

JP3 – When this port set us as “Host” or “last devices” please set up jump as pin 1, and 2. Other set up as pin 3 and 4.

JP4 – pin 1 and pin 2 – RS-422 p2p, RS-422 multi drop, and RS-485 4 wire  
pin 3 and pin 4 – RS-485 2 wire

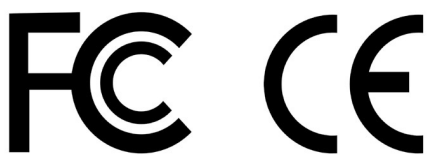
JP1 – pin 1 and pin 2 – RS-422 p2p  
pin 3 and pin 4 – RS-422 multi drop, RS-485 4 wire, and RS-485 2 wire

Port 2 –

JP5 – when this port set us as “Host” or “last devices” please set up jump as pin 1, and 2. other set up as pin 3 and 4

JP6 – pin 1 and pin 2 – RS-422 p2p, RS-422 multi drop, and RS-485 4 wire  
pin 3 and pin 4 – RS-485 2 wire

JP2 – pin 1 and pin 2 – RS-422 p2p  
pin 3 and pin 4 – RS-422 multi drop, RS-485 4 wire, and RS-485 2 wire



This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operations.